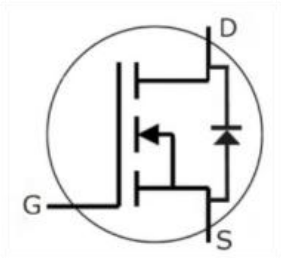


S3M007075V1K-A

TriQSiC™750V Silicon Carbide Power MOSFET G3 (N Channel Enhancement)

Features

- High speed switching
- Very low switching losses
- High blocking voltage with low on-resistance
- Temperature independent turn-off switching losses
- Halogen free, RoHS compliant



Benefits

- Cooling effort reduction
- Efficiency improvement
- Reduced cooling requirements
- Increased power density
- Increased system switching frequency

Part number	Die size (W x L) mm
S3M007075V1K-A	4.8 * 6.3

Applications

- EV motor drive
- PV string inverters
- Solar power optimizer
- Switch mode power supplies

Table 1 Key performance and package parameters

Type	V _{DS}	I _{DS} (T _C =25°C, R _{th(j-c),max} ≤0.26°C/W)	R _{DS(on), typ} (V _{GS} = 18V, I _D = 96A, T _J = 25°C)	T _{J,max}
S3M007075V1K-A	750V	219A	7.5mΩ	175°C

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1、Maximum ratings

Table 2 Maximum rating ($T_c = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Value	Unit	Test Conditions	Note
$V_{DS,max}$	Drain source voltage	750	V	$V_{GS} = 0V, I_D = 100\ \mu A$	
$V_{GS,max}$	Gate source voltage	-8 /+22	V	Absolute maximum values	Note1
$V_{GS,pulse}$	Gate-source voltage,max. transient voltage	-10 /+25	V	$t_p \leq 0.5\mu s, D < 0.01$	
V_{GSop}	Gate source voltage	-4 /+18	V	Recommended operational values	
I_D	Continuous drain current	219	A	$V_{GS} = 18V, T_c = 25^\circ\text{C}$	
		155		$V_{GS} = 18V, T_c = 100^\circ\text{C}$	
$I_{D(pulse)}$	Pulsed drain current	656	A	Pulse width $t_p = 100\mu s$ limited by $T_{J,max}$	
T_J, T_{stg}	Operating Junction and storage temperature	-55 to +175	$^\circ\text{C}$		

Note 1: when using MOSFET Body Diode $V_{GS,max} = -4 / +22V$

2、Electrical characteristics

2.1 Static characteristics

Table 4 Static characteristics (Tc = 25°C unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
$V_{(BR)DSS}$	Drain-source breakdown voltage	750	-	-	V	$V_{GS} = 0V, I_D = 100\mu A$	
$V_{GS(th)}$	Gate threshold voltage	2.3	2.8	3.6	V	$V_{DS} = V_{GS}, I_D = 35mA$	Fig.11
		-	2.1	-	V	$V_{DS} = V_{GS}, I_D = 35mA,$ $T_J = 175^\circ C$	
I_{DSS}	Zero gate voltage drain current	-	1	10	μA	$V_{DS} = 750V, V_{GS} = 0V$	
I_{GSS}	Gate source leakage current	-	-	100	nA	$V_{GS} = 18V, V_{DS} = 0V$	
$R_{DS(on)}$	Current drain-source on-state resistance	-	9	15	m Ω	$V_{GS} = 15V, I_D = 96A$	Fig.4,5,6
		-	12.5	-		$V_{GS} = 15V, I_D = 96A,$ $T_J = 175^\circ C$	
		-	7.5	12		$V_{GS} = 18V, I_D = 96A$	
		-	12	-		$V_{GS} = 18V, I_D = 96A,$ $T_J = 175^\circ C$	
g_{fs}	Transconductance	-	87	-	S	$V_{DS} = 20V, I_D = 96A$	Fig.7
		-	77	-		$V_{DS} = 20V, I_D = 96A,$ $T_J = 175^\circ C$	
$R_{g,int}$	Internal gate resistance	-	10	-	Ω	$V_{AC} = 25mV, f = 1MHz,$ open drain	

2.2 Dynamic characteristics

Table 5 Dynamic characteristics (Tc = 25°C unless otherwise specified)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
C_{iss}	Input capacitance	-	6776	-	pF	$V_{DS} = 500V, V_{GS} = 0V$ $T_J = 25^\circ C, V_{AC} = 25mV$ $f = 100kHz$	Fig.17,18
C_{oss}	Output capacitance	-	438	-			
C_{rss}	Reverse capacitance	-	23	-			
E_{oss}	Coss stored energy	-	66	-	μJ		Fig.16

750V SiC Power MOSFET

Q_{gs}	Gate source charge	-	80	-	nC	$V_{DS} = 500V, V_{GS} = -4/+18V$ $I_D = 96A$	Fig.12
Q_{gd}	Gate drain charge	-	52	-			
Q_g	Gate charge	-	262	-			

2.3 Switching characteristics

Table 6 Dynamic characteristics($T_c = 25^\circ C$ unless otherwise specified,the data comes from TO-247-4L)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
E _{on}	Turn on switching energy	-	1605	-	μJ	V _{DS} = 500V, V _{GS} = -4/+18V I _D = 96A, R _g = 2.5Ω, L = 16.7μH	
E _{off}	Turn off switching energy	-	1256	-			
t _{d (on)}	Turn on delay time	-	30	-	ns		
t _r	Rise time	-	61	-			
t _{d (off)}	Turn off delay time	-	140	-			
t _f	Fall time	-	28	-			

Table 7 Body diode characteristics(The data comes from TO-247-4L)

Symbol	Parameter	Min.	Typ.	Max.	Unit	Test Conditions	Note
V_{SD}	Diode forward voltage	-	3.5	-	V	$V_{GS} = -4V, I_{SD} = 48A$	Fig.8,9, 10
		-	3.1	-	V	$V_{GS} = -4V, I_{SD} = 48A$ $T_J = 175^\circ C$	
I_S	Continuous diode forward current	-	152	-	A	$V_{GS} = -4V, T_c = 25^\circ C$	Note2
t_{rr}	Reverse recovery time	-	60	-	ns	$V_R = 500V, V_{GS} = -4V$ $I_D = 96A$ $di/dt = 2286A/\mu s$ $T_J = 175^\circ C$	
Q_{rr}	Reverse recovery charge	-	873	-	nC		
I_{rrm}	Peak reverse recovery current	-	37	-	A		

Note2 : When using SiC Body Diode the maximum recommended $V_{GS} = -4 V$

3、Electrical characteristic diagrams

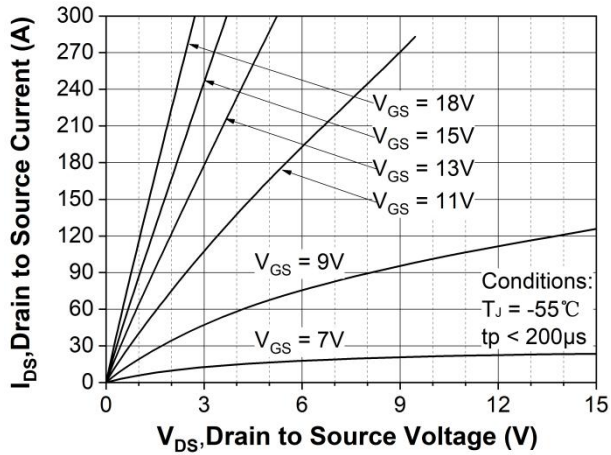


Figure 1. Output characteristics $T_J = -55^\circ\text{C}$

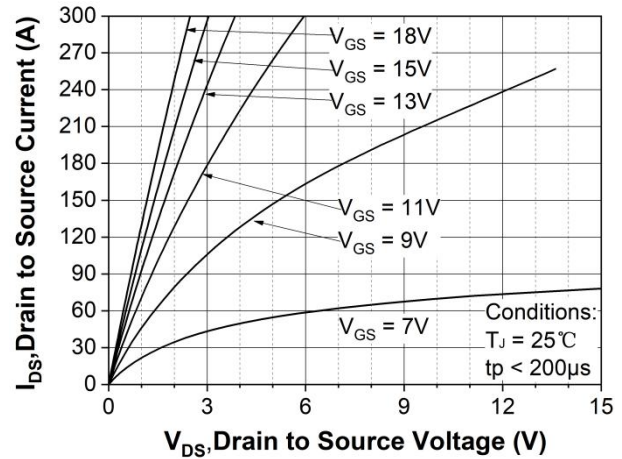


Figure 2. Output characteristics $T_J = 25^\circ\text{C}$

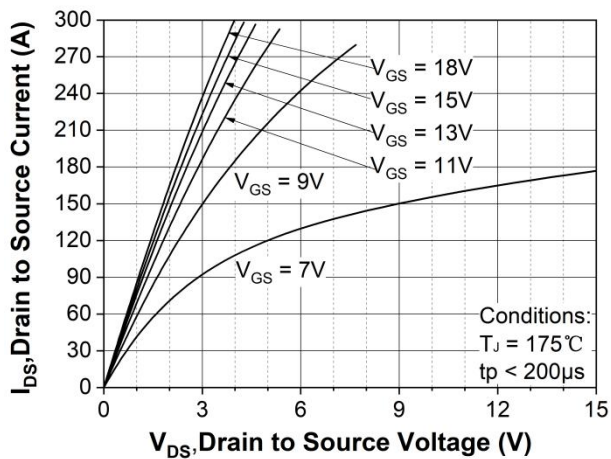


Figure 3. Output characteristics $T_J = 175^\circ\text{C}$

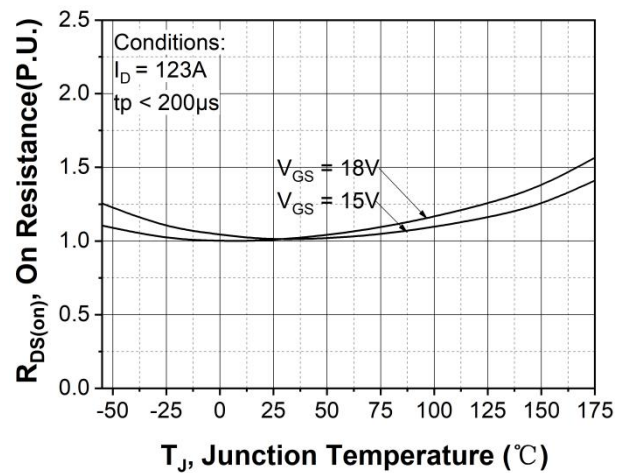


Figure 4. Normalized on-resistance vs. temperature

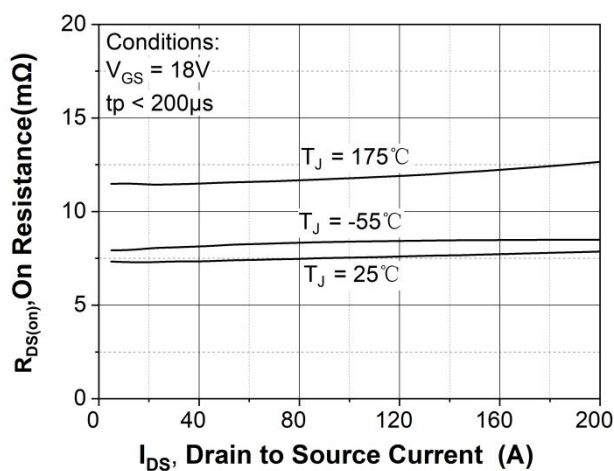


Figure 5. On-resistance vs. drain current
for various temperatures

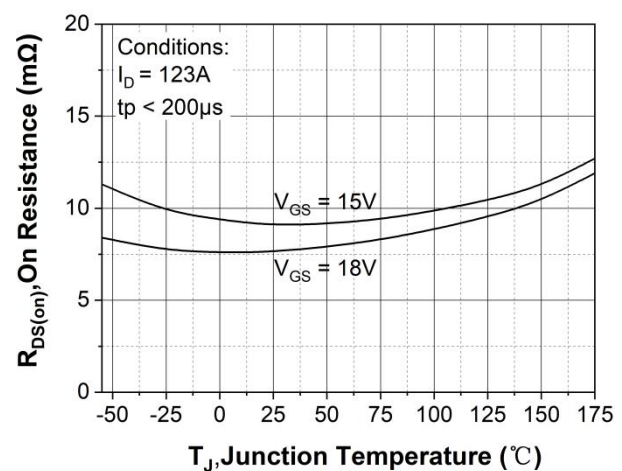


Figure 6. On-resistance vs. temperature
for various gate voltages

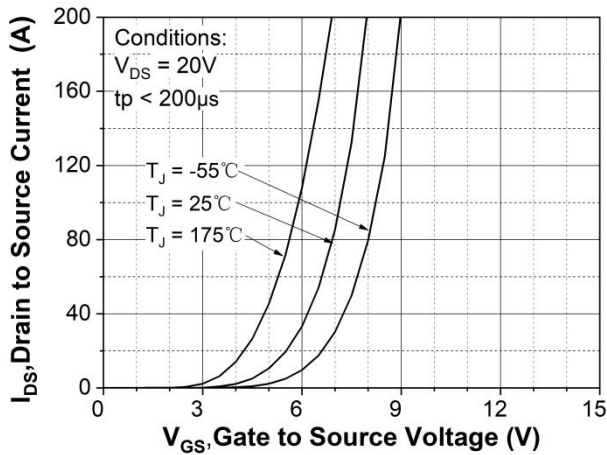


Figure 7. Transfer characteristic for various junction temperatures

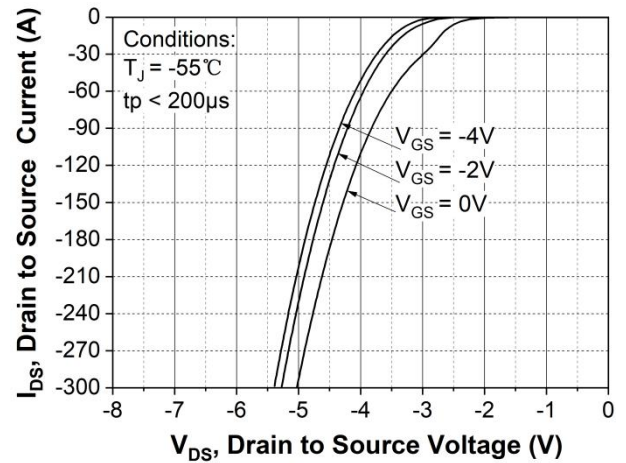


Figure 8. Body diode characteristic at $T_J = -55^\circ\text{C}$

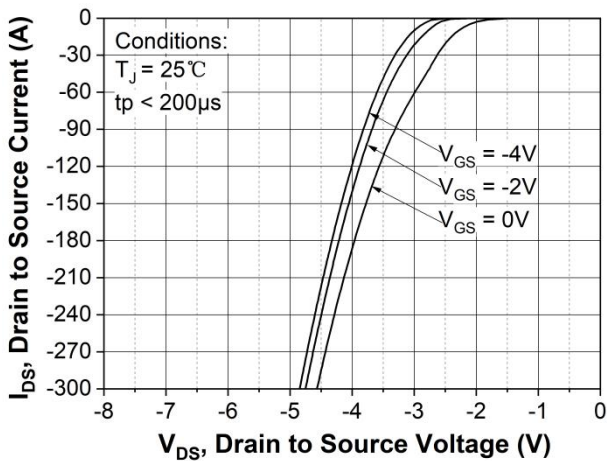


Figure 9. Body diode characteristic at $T_J = 25^\circ\text{C}$

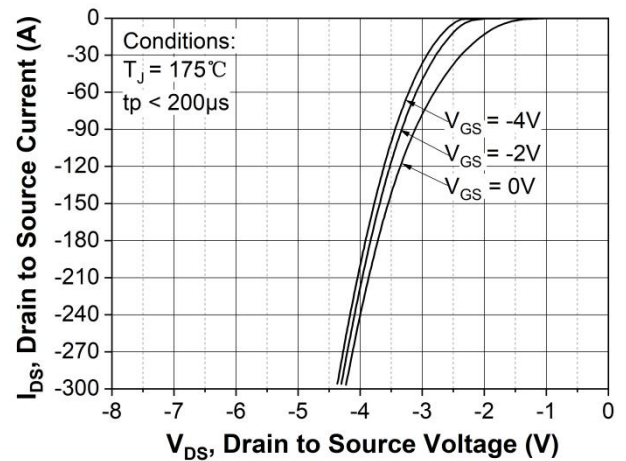


Figure 10. Body diode characteristic at $T_J = 175^\circ\text{C}$

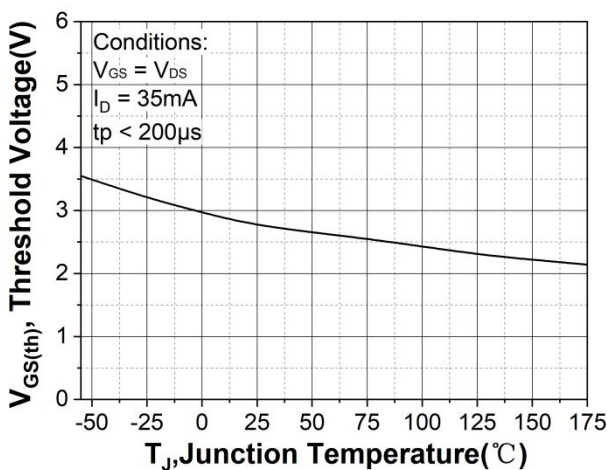


Figure 11. Threshold voltage vs. temperature

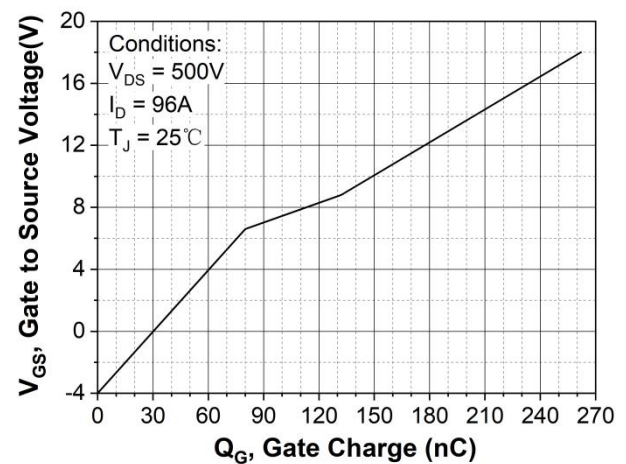


Figure 12. Gate charge characteristic

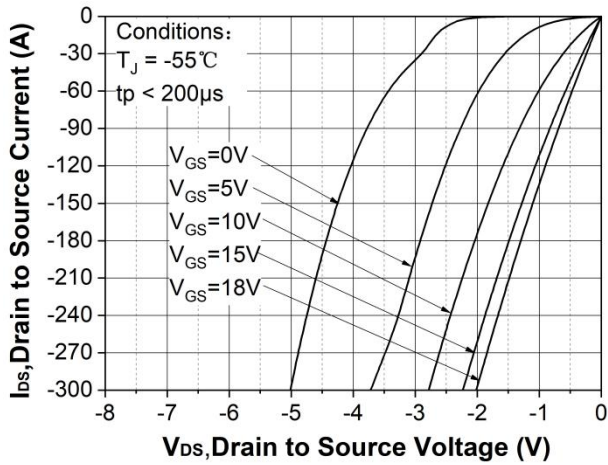


Figure 13. 3rd quadrant characteristic at $T_J = -55\text{ }^{\circ}\text{C}$

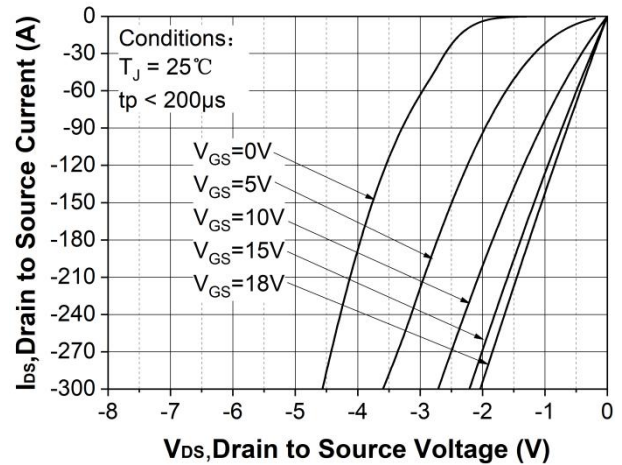


Figure 14. 3rd quadrant characteristic at $T_J = 25\text{ }^{\circ}\text{C}$

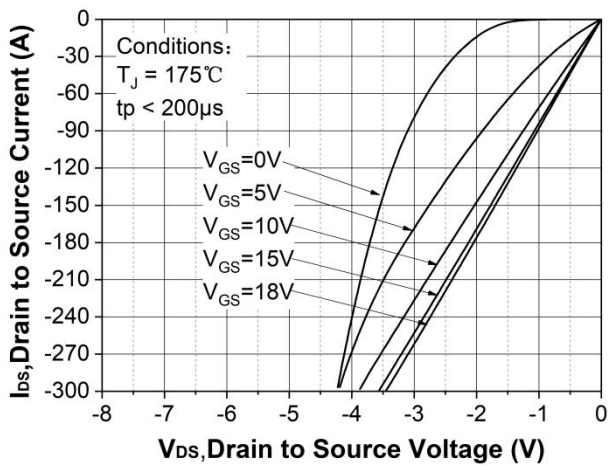


Figure 15. 3rd quadrant characteristic at $T_J = 175\text{ }^{\circ}\text{C}$

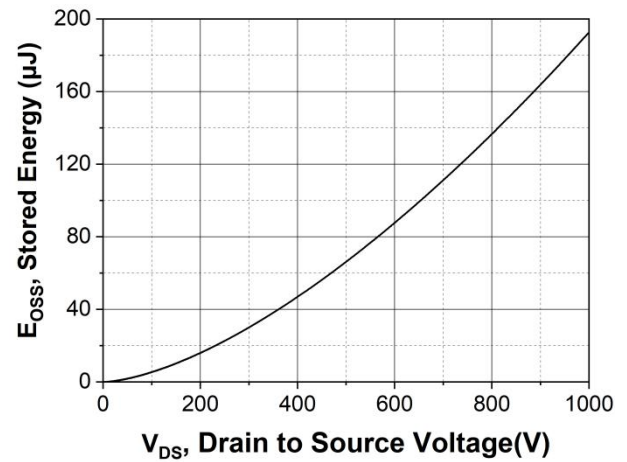


Figure 16. Output capacitor stored energy

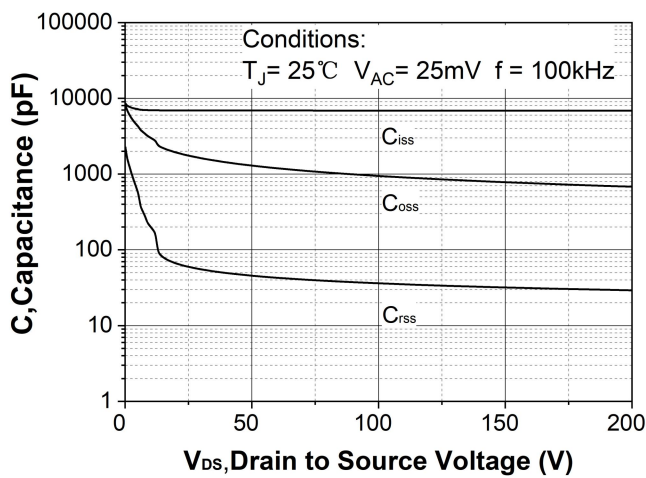


Figure 17. Capacitances vs. drain-source voltage (0 - 200V)

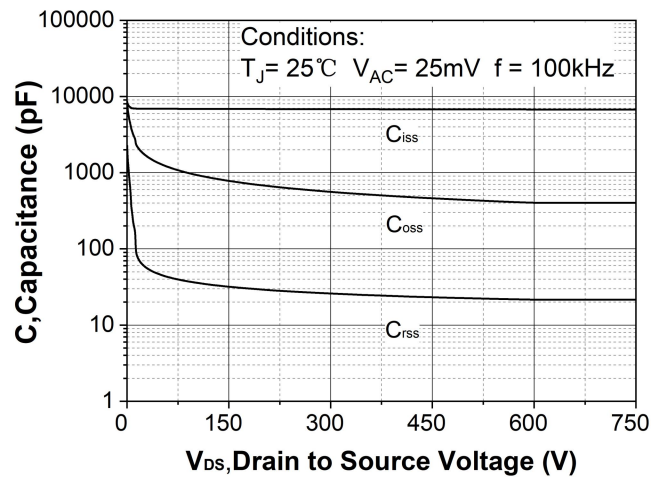


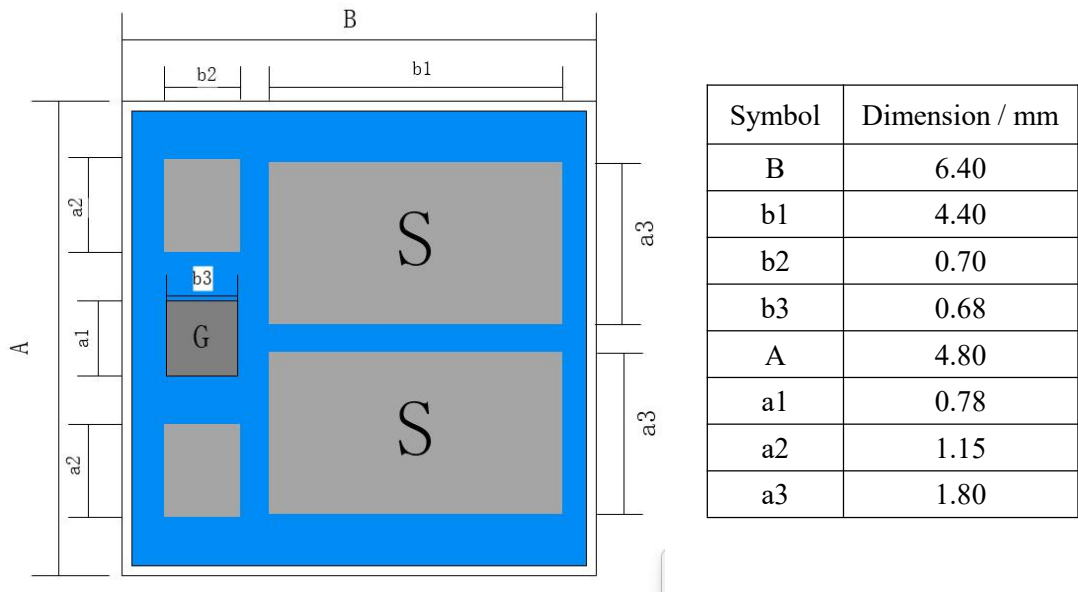
Figure 18. Capacitances vs. drain-source voltage (0 - 750V)

4、Mechanical parameters

4.1 Dimensions and metallization

Parameter	Typical value	Unit	Metallization
Die size (W x L)	4.80 * 6.30	mm	
Gate pad size (W x L)	0.78 * 0.68	mm	
Source pad size (W x L)	1.80 * 4.40	mm	
Kelvin pad size (W x L)	0.70 * 1.15	mm	
Die thickness	130 ± 20	μm	
Top side (gate & kelvin source) metallization	4/4/12	μm	AlCu/Ni/Cu
Top side (power source) metallization	4/4/12	μm	AlCu/Ni/Cu
Back side (drain) metallization	1	μm	Ag

4.2 Layout



5、 Test conditions

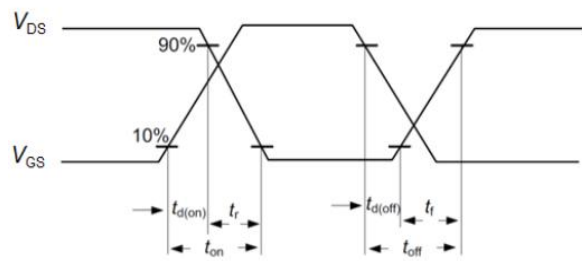


Figure A. Definition of switching times

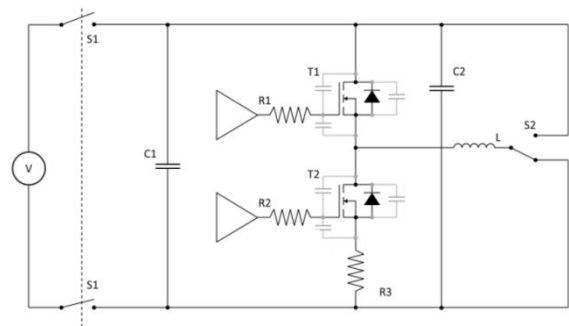


Figure B. Dynamic test circuit

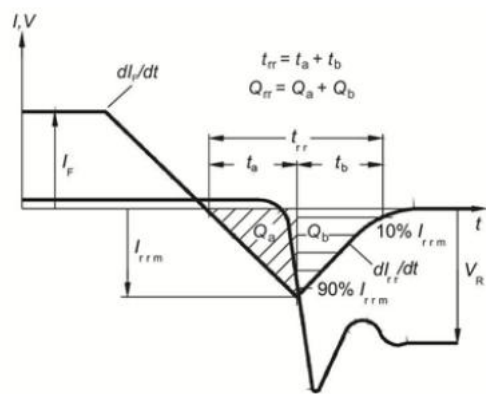


Figure C. Definition of body diode switching characteristics

Revision history

Document version	Date of release	Document stage	Description of changes
V01_00	2026-06-26	Final	--

Attention

1. RoHS compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/ EC (RoHS2), as implemented January 2, 2013.

2. REACH compliance

REACH substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact a Sichain representative to insure you get the most up-to-date REACH SVHC Declaration. REACH banned substance information (REACH Article 67) is also available upon request.

3. With respect to information regarding the application of the product, Sichain hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party.

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8. For use of our products in applications requiring a high degree of reliability (as exemplified below), please contact and consult with a Sichain representatives, for example but not limited to: transportation equipment, primary communication equipment, traffic lights, fire/crime prevention, safety equipment, medical systems, and power transmission systems.